

MICROWELL ARRAY CHIP AND ITS MANUFACTURING METHOD

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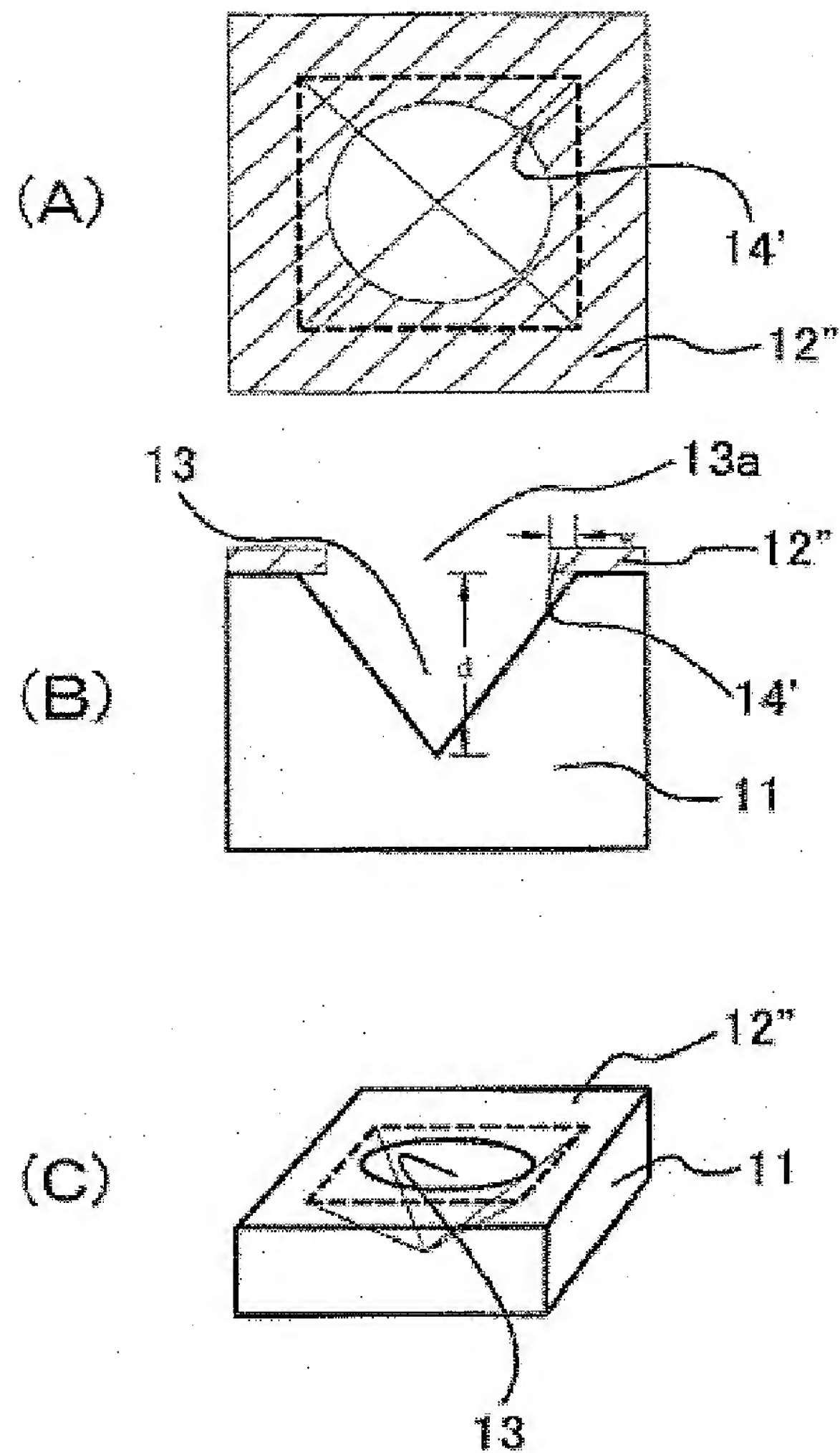
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Abstract of WO2005069001

A microwell array chip comprising microwells in one major surface of a substrate. Each microwell has such a shape and dimensions that it contains only one living cell. On the major surface of the substrate where the openings of the microwells are provided, markers for the microwells are provided. Another microwell array chip comprising microwells in one major surface of a substrate. Each microwell has such a shape and dimensions that it contains only one living cell. A projection portion narrowing the opening of each microwell is disposed in the opening. A method for manufacturing the microwell array chip comprises a step of forming a film at least on one major surface of a substrate, a step of applying a resist to the formed film, a step of exposing the resist surface through a mask having a microwell pattern and removing the uncured portion of the resist, a step of making a hole of microwell array shape by etching the film and the exposed portion of the substrate, and a step of removing the resist. Further, another microwell array chip of a silicon having microwells in each of which one living cell of a subject is contained. Each microwell has such a shape and dimensions that it contains only one living cell.



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